



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2841
Examiner: Ishwarbhai B Patel

In Re PATENT APPLICATION Of:

Applicants : Norio TAKAHASHI)

Appln. No. : 10/775,232)

Filed : February 11, 2004)

For : MODULE CIRCUIT BOARD FOR)
SEMICONDUCTOR DEVICE)
HAVING BARRIERS TO ISOLATE)
I/O TERMINALS FROM SOLDER)

AMENDMENT

Attorney Ref.: F97ED0740-DIV3)

April 13, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, Va 22313-1450

Sir:

In response to the Examiner's Office Action mailed December 22, 2006, which has been extended to expire on March 22, 2007 by a Petition for a one-month Extension of Time filed herewith, please amend the above-identified application as follows.